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BISIMULATION CAN'T BE TRACED: PRELIMINARY REPORT

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Bisimulation is the primitive notion of equivalence between concurrent processes in Milner's Calculus of Communicating Systems (CCS); there is a nontrivial game-like protocol for distinguishing nonbisimular processes. In contrast, process distinguishability in Hoare's theory of Communicating Sequential Processes (CSP) is determined soley on the basis of traces of visible actions. We examine what additional operations are needed to explain bisimulation similarly-specifically in the case of finitely branching processes without silent moves. We formulate a general notion of Structured Operational Semantics for processes with Guarded recursion (GSOS), and demonstrate that bisimulation does not agree with trace congruence with respect to any set of GSOS-definable contexts. In justifying the generality and significance of GSOS's, we work out some of the basic proof theoretic facts which justify the SOS discipline. Keywords: ____

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Bisimulation Can't Be Traced:

Preliminary Report [1]

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1   Introduction

Milner's CCS [18], [19] and Hoare's CSP [16], [17] share the premise that the meaning of a process is fully determined by a *synchronization tree*, namely, a rooted, unordered tree whose edges are labeled with symbols denoting basic *actions* or events. These trees are typically specified by a Structured Operational Semantics (SOS) in the style of [23] or by some other effective description, and so are in fact recursively enumerable trees. Both theories further agree that synchronization trees are an *over*specification of process behavior, and certain distinct trees must be regarded as equivalent processes. The notable difference in the theories is that bisimulation yields finer distinctions among synchronization trees.

In CSP, process distinctions can be understood as based on observing *traces*, namely, *maximal* sequences of visible actions performed by a process. Two trees are *trace equivalent* iff they have the same set of traces. Given any set of operations on trees, *trace congruence* is defined to be the coarsest congruence with respect to the operations which refines trace equivalence. Thus, two CSP processes are distinguished iff each can be used in a single CSP context which yields a different set of traces depending on which of the two processes is used. This explanation of when two synchronization trees are to be identified is thoroughly elaborated in Hennessy and DeNicola's *test equivalence* system [10]. On the other hand, two CCS processes are distinguished according to an "interactive" game-like protocol called *bisimulation*. Indistinguishable CCS processes are said to be *bisimular*.

Figure 1: *Trace equivalent but not trace congruent.*

A standard example is the pair of trees (Figure 1) $a(b + c)$ and $(ab + ac)$ which are trace equivalent, but not CSP trace congruent, *viz.*, in CSP (and also CCS) they are distinct processes. Similarly, the trees of (Figure 2)

$$(abc + abd) \text{ and } a(bc + bd) \tag{1}$$

are CSP trace congruent but not bisimular, *viz.*, equal in CSP but distinct in CCS [24], [6]. The trace-based approach is developed in [7], [17], [21], [10]. Bisimulation-based systems include [19], [20], [2], [4], [5], [3].

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Figure 2: *CSP trace congruent but not bisimular.*

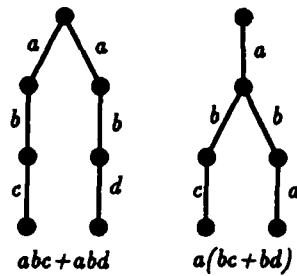The idea of an "silent" (aka "hidden" or "τ-") action plays an important role in both CSP and CCS theories, but creates significant technical problems. In this paper we assume for simplicity that *there is no silent action*. We expect that our conclusions will generally apply when silent actions can occur, but this remains to be verified.

In the absence of silent action, bisimulation is known to be a congruence with respect to all the operations of CSP/CCS, and Milner has argued extensively that in this case bisimulation yields the finest appropriate notion of the behavior of concurrent processes based on synchronization trees. Although there is some ground for refining synchronization trees further (*cf.* [8]), we shall accept the thesis that bisimular trees are not to be distinguished. Thus, we admit below only operations with respect to which bisimulation remains a congruence. Since bisimular trees are easily seen to be trace equivalent, it follows in this setting that bisimulation refines any trace congruence. Our results focus on the converse question of whether further identifications should be made, *i.e.*, whether nonbisimular processes are truly distinguishable in their observable behavior.

We noted that a pair of nonbisimular trees T_1, T_2 can be distinguished by an "interactive" protocol. The protocol *itself* can be thought of a new process $P[T_1, T_2]$. One might suppose that in a general concurrent programming language, it would be possible to define the new process too, and that success or failure of P running on a pair T_1, T_2 would be easily visible to an observer who could observe traces.

However, CSP and CCS operations are very similar, and the example of Figure 2 above shows that bisimulation is a strictly finer equivalence than trace congruence with respect to CSP/CCS operations. It follows that the contexts P distinguishing nonbisimular processes by their traces *are not definable using the standard CSP/CCS operations*; if they were, nonbisimularity could be reduced to trace distinguishability. Namely, any pair of nonbisimular trees T_1, T_2 would also be trace distinguishable by plugging them in for X in $P[X, T_1]$ and observing the "success" trace when T_1 is plugged in, but not when T_2 is plugged in.

Thus, we maintain that implicit in concurrent process theory based on bisimulation is an-
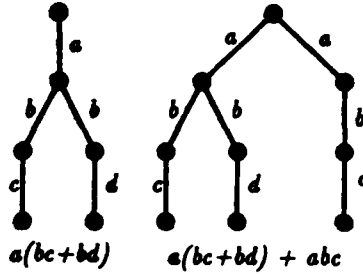
Figure 3: *GSOS congruent but not bisimular.*

other "interactive" kind of *metaprocess*, which the formalisms of CSP/CCS are inadequate to define! Our question is

> *What further operations on CCS/CSP terms are needed so that protocols reducing nonbisimularity to trace distinguishability become definable?*

In the remainder of the paper, we argue that bisimulation *cannot* be reduced to a trace congruence with respect to any *reasonably structured* system of process constructing operations. The implications of this conclusion are discussed in the final Section 7.

In particular, we formulate in Section 3 a general notion of a system of processes given by structured rules for transitions among terms with guarded recursion—a *GSOS* system. We also indicate why the focus on guarded recursion is both necessary and appropriate. All previously formulated systems of bisimulation-respecting operations are definable by GSOS's. Even rules with negative antecedents are allowed in GSOS's. On the other hand. we indicate in Section 4 that any of the obvious further relaxations of the conditions defining GSOS's can result in systems which are ill-defined or fail to respect bisimulation. Thus, we believe that GSOS definability provides a generous and mathematically invariant constraint on what a reasonably structured system of processes might be.

Definition 1 *Two processes are GSOS trace congruent iff they yield the same traces in all GSOS definable contexts.*

Our main result is that bisimulation—even restricted to finite trees—is always a *strict* refinement of GSOS trace congruence. Specifically, we develop in Section 5 a modal logical characterization of GSOS trace congruence similar to the characterization of bisimulation given by Hennessy and Milner [15] and use it to prove:
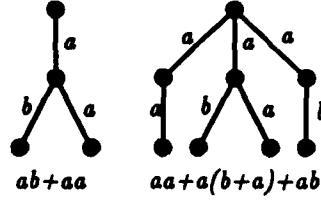
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Figure 4: *CSP trace congruent but not GSOS trace congruent.*

Theorem 1 *The nonbisimular trees $a(bc + bd)$ and $a(bc + bd) + abc$ (Figure 3) are GSOS trace congruent.*

We remark that GSOS congruence is a strict refinement of CSP congruence:

Theorem 2 *The processes $aa + ab$ and $aa + ab + a(a + b)$ (see Figure 4) are CSP trace congruent [10, axiom (D5), p. 99] but not GSOS trace congruent.*

Abramsky [1] independently raised the question of how to test distinguishability of non-bisimular processes and formalized the operational behavior of a set of protocols which do capture bisimulation. In Section 6 we offer a similar system for the task, slightly improved in certain respects. Our thesis that no reasonably structured system can capture bisimulation implies that both these systems must lack some important structured features. We also examine the nature of these flaws in detail in Section 6.

2 Trees and Modal Formulas

The simplest tree consists of just a root without edges. It is the "successfully stopped" process, 0. If a is an action and P is a tree, then aP is the tree with a fresh root and a single edge, labeled with a, from its root to the root of P (Figure 5). The intention is that aP corresponds to the process which performs action a and then behaves like P. Thus $a0$ is the tree with one edge labeled a corresponding to the process whose only behavior is to do action a and stop. We ambiguously write just "a" for $a0$. If P and Q are synchronization trees, let $P + Q$ be the tree obtained by identifying the roots of (disjoint copies of) P and Q (Figure 6). The intention is that $P + Q$ models the nondeterministic process which can behave like either P or Q.

Figure 5: *Tree for aP.*

Figure 6: *Tree for $P + Q$.*

This definition of $+$ and 0 explains why both CCS (branching) and CSP (linear) theories accept the familiar axiom

$$p + 0 = p .\tag{2}$$

Since trees are unordered, both theories also include the equally familiar axioms

$$(p + q) + r = p + (q + r), \quad p + q = q + p .\tag{3}$$

Now we note that the tree $a + a$ corresponds intuitively to a process which has two different ways of doing action a and stopping. But the intuition that an action is a minimal detectable event suggests that the theory should disallow detection of the *way* a basic event occurred—all that can be detected is the occurrence of the event itself. Thus in both CSP and CCS, $a = a + a$, and more generally,

$$p = p + p ,\tag{4}$$

is also accepted as a valid axiom.

The "interactive" protocol defining bisimulation appears in many of the references and we omit it. We remark that two *finite* trees are bisimular iff they are provably equal as a consequence of equations (2)–(4). For our purposes, the most useful formulation of bisimulation is in terms of Hennessy-Milner-logic (HML) formulas [15]:

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Definition 2 An *HML formula* is given by the following grammar:

$$\varphi ::= \text{tt} \mid \text{ff} \mid \langle a \rangle \varphi \mid [a]\varphi \mid \varphi \vee \varphi \mid \varphi \wedge \varphi \ .$$

For any synchronization tree T, the satisfaction relation, $T \models \varphi$, is defined as usual for modal logic and Kripke models (*cf.* [11], [25], [12], [13], or [14]).

A fundamental result of [15] is that two *finitely branching* synchronization trees are bisimular iff they satisfy exactly the same HML formulas.

In fact, both Abramsky's and our operational rules in Section 6 for systems where bisimulation coincides with trace congruence are essentially systems for calculating whether a computably finitely branching synchronization tree satisfies an HML formula.

3 Guarded Terms

Synchronization trees defined in CCS using unguarded recursion may be infinitely branching, corresponding to what are called "unboundedly nondeterministic" processes. Bisimulation between such CCS-definable trees cannot—on purely recursion-theoretic grounds based on degree of undecidability—match trace congruence with respect to any set of effective operations on trees. (Actually, this is an interesting story to work out in detail, but we save that for another paper.)

The high degree of undecidability of bisimulation arises from unbounded nondeterminism. Unbounded nondeterminism is a source of other theoretical difficulties as well, notably that the desired operations on processes are not continuous with respect to any useful known topology. For example, there are simple examples of synchronization trees whose finite subtrees from the root are identical, but which are nevertheless not bisimular. For this reason, restrictions are generally imposed on recursive definitions of processes. In CCS, "guarded" recursion is singled out as attractive, and in CSP and the test-equivalence system of [10], unguarded recursions are treated as though they diverged (with an infinite sequence of silent moves). The essence of these restrictions is to ensure that definable trees behave like *computably finitely branching* trees, *i.e.*, there is an effective procedure which, given (a term denoting) a tree node M, computes the finite set $\left\{ N : M \xrightarrow{a} N \right\}$.

For finitely branching trees, trace congruence (including infinite traces) coincides with finite-trace congruence, and as we noted two such trees are bisimular iff they satisfy the same set of HML formulas. Since it is decidable whether a computably finitely branching tree satisfies an HML formula, it follows that bisimulation between such trees is at most Π_1^0, as is the degree of trace congruence with respect to effective operations on such trees. Thus, on recursion-theoretic grounds, finite-trace congruence with respect to some suitable

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set of operations could equal bisimulation. As noted, this is indeed possible as we show in Section 6. Nevertheless, no reasonably structured operational system can do the job. as we now explain precisely.

We consider theories of synchronization trees described in the standard way by algebraic terms with fixed point operations.

Definition 3 *Process terms*, M, are given by the grammar:

$$M ::= X \mid aM \mid \text{op}(M, \ldots, M) \mid \text{fix}\, X.M \tag{5}$$

where X ranges over synchronization tree variables, a ranges over action symbols, and "op" ranges over operation symbols (of varying arity). M is *guarded* iff, for each subterm $\text{fix}\, X.N$, each occurrence of X in N is within the scope of an a-prefixing operator.

Note that all terms not containing fixed points are guarded. The unary operator $a(\cdot)$ is distinguished from the other operator symbols since it plays a special role in the definition of guarded terms. It will be required to have the same meaning in all GSOS's, namely a-prefixing.

Definition 4 A *Structured Transition Rule* (STR) is a rule of the form:

$$\frac{\left\{X_i \overset{a_{ij}}{\to} Y_{ij} \mid 1 \leq j \leq m_i\right\}_{i=1}^{l}, \quad \left\{X_i \overset{b_{ij}}{\nrightarrow} \mid 1 \leq j \leq n_i\right\}_{i=1}^{l}}{\text{op}(X_1, \ldots, X_l) \overset{a}{\to} M}$$

where the variables are all distinct, $l \geq 0$ is the arity of op; $m_i, n_i \geq 0$, and M is a guarded term whose free variables are contained in the set of X_i's and Y_{ij}'s. (M need not contain all these variables.) The symbol op is called the *principal operator* of the rule.

The antecedents of the form $X \overset{a}{\to} Y$ are called *positive*, and are satisfied when X and Y are instantiated with processes such that Y is an a-child of X's synchronization tree (*cf.*Definition 7 below). The others, of the form $X \overset{b}{\nrightarrow}$, are called *negative*; they are satisfied when X is instantiated by a process with no a-children at its root. The rule is *positive* if all the antecedents are positive.

Note that every X_i occurring in the antecedent of an STR must occur as an argument of the principal operator in the consequent, but not every argument of the principal operator need occur in the antecedent.

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Definition 5 *Fixed point rules* are of the form:

$$\frac{M[X := \text{fix}\, X.M] \xrightarrow{a} Y}{\text{fix}\, X.M \xrightarrow{a} Y} \tag{6}$$

where $M[X := N]$ denotes substitution of N for X in M, with renaming to avoid capture of free variables in N. *Guarded fixed point rules* are fixed point rules in which the term $\text{fix}\, X.M$ is guarded.

Many of our results are unaffected if an arbitrary family of what we call *δ-rules* of the form

$$\mathbf{c}_i \xrightarrow{a} \mathbf{c}_j$$

are allowed, involving (a possibly uncountable number) of constants $\mathbf{c}_i$.

All GSOS's have all guarded fixed point rules (6), and so these are not mentioned explicitly in the next definition.

Definition 6 A *GSOS rule system* is given by a finite alphabet of actions a, a finite number of STR's, and an arbitrary set of δ-rules. There must, for each a, be a unique rule whose consequent is of the form $aX \xrightarrow{a} M$, and this rule must be

$$aX \xrightarrow{a} X \tag{7}$$

Definition 7 *An* instantiation, ε, *is a map from variables to guarded terms. If P is a guarded term, then $P[\varepsilon]$ denotes the simultaneous substitution of $\varepsilon(X)$ for each free occurrence of X in P.*

Definition 8 *A system of binary relations $\xrightarrow{a}$ on guarded terms, indexed by actions a, agrees with a set of rules iff:*

- *Whenever an instantiation by ε of the antecedents of a rule is true of the relation, then the instantiation of the consequent by ε is true as well.*

- *Whenever $P \xrightarrow{a} Q$ is true, then there is a rule r and an instantiation ε such that $P \xrightarrow{a} Q$ is the instantiation of the consequent of r by ε, and the instantiations of the antecedents of r by ε are true.*

Theorem 3 *For any GSOS system, there is a unique system of arrow relations indexed by actions which agrees with the STR's of the GSOS plus the guarded fixed point rules (6).*

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Proof: A routine structural induction and slightly less routine fixed point induction on guarded terms. Details omitted. □

For example, Milner's SCCS operators [19] are defined by STR's:

$$\frac{X \xrightarrow{a} Y}{X + X' \xrightarrow{a} Y} \ , \ \frac{X' \xrightarrow{a} Y}{X + X' \xrightarrow{a} Y}$$

$$\frac{X \xrightarrow{a} Y}{X \lceil A \xrightarrow{a} Y \lceil A} \ (a \in A \subseteq \mathsf{Act})$$

$$\frac{X \xrightarrow{a} Y, \ X' \xrightarrow{b} Y'}{X \times X' \xrightarrow{a \cdot b} Y \times Y'}$$

The simple interleaving product of CCS is given by:

$$\frac{X \xrightarrow{a} Y}{X|X' \xrightarrow{a} Y|X'} \ , \ \frac{X' \xrightarrow{a} Y}{X|X' \xrightarrow{a} X|Y}$$

Sequential composition of processes, ";", may (and must) be defined using negative rules:

$$\frac{X \xrightarrow{a} Y}{X; X' \xrightarrow{a} Y; X'} \qquad \frac{X' \xrightarrow{b} Y', \ \left\{ X \xrightarrow{a} \ | \ a \in \mathsf{Act} \right\}}{X; X' \xrightarrow{b} Y'}$$

Thus, the operational rules assigning synchronization trees to CCS/CSP/ACP/MIEJE terms easily fit the GSOS framework.

In fact, STR's go beyond the kind of SOS rules needed for CCS in two respects—use of negation and use of *copying*. Namely, there can be more than one antecedent about the behavior of the same subprocess in an STR. For example,

$$\frac{X \xrightarrow{a} Y, X \xrightarrow{b} Y'}{\text{a-if-b}(X) \xrightarrow{a} \text{a-if-b}(Y)}$$

involves two "copies" of process X in the antecedent. Substituting for X in a-if-b($X + b$) trace distinguishes the two trees in Theorem 2 (Figure 4). This example also shows that GSOS congruence strictly refines Phillips *refusal testing* congruence [22] which he was led to develop by considerations similar to ours and Abramsky's. Note that neither recursion nor negative rules were needed in this example. De Simone [9] shows that without negation *or* copying, the only GSOS definable operators are already CCS definable.

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4 Why GSOS?

Keeping to the GSOS discipline provides structural induction as a proof technique, as indicated for Theorem 3. In addition, GSOS's guarantee:

Theorem 4 *If a set of δ-rules defines a set of computably finitely branching trees, then the trees definable in any GSOS system with these δ-rules are also computably finitely branching.*

Theorem 5 *Bisimulation is a congruence with respect to all GSOS contexts.*

There are many technical restrictions in our definition of a GSOS rule, and it is natural to ask if they can be relaxed. We indicate how various relaxations may break the key properties of GSOS systems. Note that some systems with non-GSOS rules enjoy the good properties of GSOS systems; however, this is not immediate from the syntactic specifications of these systems. We maintain that GSOS's represent essentially the most general family of systems satisfying Theorems 3-5. The two properties which non-GSOS rules often violate are:

1. The existence of a unique system of arrow relations, $\xrightarrow{a}$, agreeing with the rules.

2. The guarantee that bisimulation is a congruence.

The disjointness of the variables on the right and left sides of arrows in the antecedent is required to guarantee the existence of an arrow relation. Consider a system including the three operators α, β, and γ defined as follows:

$$\frac{X \xrightarrow{a} Y_1, Y_1 \xrightarrow{a} Y_2}{\alpha(X) \xrightarrow{a} 0}$$

$$\frac{X \xrightarrow{a}}{\beta(X) \xrightarrow{a} 0}$$

$$\gamma \xrightarrow{a} \beta(\alpha(\gamma))$$

It is not hard to show that there is no arrow relation which agrees with these rules. In particular, $\alpha(\gamma)$ can move iff it cannot move.

Other ways to use variables for pattern-matching allow us to distinguish between bisimular processes. For example, the following rule has two uses of Y.

$$\frac{X_1 \xrightarrow{a} Y, \quad X_2 \xrightarrow{a} Y}{\delta(X_1, X_2) \xrightarrow{a} 0}$$

The context $\delta(a0, a\cdot)$ distinguishes between the bisimular stopped processes 0 and $0 + 0$.

Other forms of pattern-matching, predictably, fail to guarantee preservation of bisimulation. For instance, if we allow the left-hand side of a consequent to look at more than the first operator of a process, we can have the rule

$$\zeta(0) \xrightarrow{a} 0$$

which again gives a context $\zeta(\cdot)$ which distinguishes between 0 and $0 + 0$.

5 Limited Modal Formulas

Definition 9 Limited modal formulas *are given by the following grammar:*

$$\varphi ::= \text{tt} \mid \text{ff} \mid [a]\text{ff} \mid \langle a \rangle \varphi \mid \varphi \wedge \varphi \mid \varphi \vee \varphi.$$

Two trees are defined to be limited modal equivalent *iff they satisfy the same set of limited modal formulas.*

That is, a limited modal formula is an HML formula with a very restricted use of the $[a]$ (necessity) modality.

Theorem 6 *For finitely branching synchronization trees, limited modal equivalence coincides with GSOS trace congruence.*

Proof: A nontrivial structural and fixed-point induction, which is omitted from the preliminary report. $\Box$

6 Global Testing Semantics of Bisimulation

Abramsky, "aim[ing] to place the cards on the table as a basis for ... discussion," [1, p. 15], develops a clear system of operational rules whose trace congruence coincides with bisimulation for finitely branching trees without silent actions. By Theorem 1, this system cannot conform to the GSOS discipline. Examining it in detail, we find:

1. There are *two sorts* of processes, those given CCS-like treatment as synchronization trees and other "test" processes which have *different* operations defined on them. For example, test processes are *not* closed under recursive definitions.

2. The inductive rules defining the operational semantics of test processes assign transitions to terms which must pattern-match on more than their outermost operator. This violates the GSOS format and makes it unclear whether the operations respect bisimulation.

3. Indeed, *bisimulation is not a congruence* on test processes, nor are bisimular test processes trace congruent.

4. The inductive rules defining the operational semantics of test processes involve *negative antecedents*. This makes it unclear whether the transition relation is even well-defined, given the presence of non-GSOS rules in the system.

5. Finally, to define the operational behavior of tests, Abramsky introduces rule-schemes which he describes as involving *global testing*. Namely, if T is a tree, let $\delta_a(T) = \{T_i | T \overset{a}{\to} T_i\}$. Then rule schemes of the following form appear:

$$\frac{\delta_a(T) = \{T_1, \ldots, T_n\}}{\exists(T) \overset{a}{\to} \bigvee_{i=1}^{n} \exists(T_i)} \tag{8}$$

Note that the number of antecedents implicitly appearing in such global-testing rules is *unbounded*.

Items 1, 2, and 3 can be repaired in an *ad hoc* way. A guarded (pun intended) use of negative antecedents can, as already demonstrated in Section 4, be admitted in well-structured systems. We now exhibit our own variation of Abramsky's system as a modest improvement which meets concerns 1-4. The global-testing rules remain, however, as an objectionable feature.

Global testing is objectionable because, in general, *bisimulation may not be a congruence with respect to operations definable with global testing*—although it *is* a congruence for our particular system. Indeed it is undecidable to determine whether bisimulation is a congruence given a finite set of GSOS-plus-global-testing rules. Even when bisimulation is a congruence, merely adding positive STRs may destroy the congruence.

Let S be any GSOS system. We will extend S to a new system $\bar{S}$, involving global-testing as well as GSOS rules, in which trace congruence coincides with bisimulation. The extension is conservative, namely, if P is a process in S, then the behavior of P in $\bar{S}$ is precisely that of P in S.

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```
true            false
and(·, ·)       or(·, ·)
pos_a(·)        nec_a(·)
Sat(·, ·)
· △ ·           · ▽ ·
```

Figure 7: Operators for Global-Testing System

The actions of $\bar{S}$ are the actions of S, plus $\{e, d, t, f, b_1, b_2, \ldots, b_n\}$ where n is a suitably large number chosen below. The operations of $\bar{S}$ are those of S, together with the operators shown in Figure 7

The first six operators are the *formula-representing operators*; there is a $pos_a(P)$ and $nec_a(P)$ for each action a of $\bar{S}$.

If F codes a HML formula φ, then $Sat(P, F)$ will compute whether or not P satisfies φ.[4] $Sat(P, F)$ takes a t-step if F codes tt, and an f-step if F codes ff; otherwise it makes an e-step, doing one step of the computation trying to see if P satisfies F, and produces an e.

Finally, the binary infix operators $\cdot \triangle \cdot$ and $\cdot \triangledown \cdot$ are are the *computational boolean* operators, which evaluate the conjunctions and disjunctions arising in the evaluation of $Sat(P, F)$.

The system codes HML formulas into bisimulation trees in the following way. Let the *bit pattern of F, bitpat(F)*, be the vector $\langle v_1, \ldots, v_n \rangle$, where $v_i = 0$ if $F \overset{b_i}{\nrightarrow}$ and $v_i = 1$ otherwise. Choose n large enough so that each connective of HML over $\bar{S}$ can be coded as a distinct bit pattern; let $code(\sigma)$ be the bit vector coding the connective σ. (The connectives are tt, ff, $\wedge$, $\vee$, and $\langle a \rangle$ and $[a]$ for each action a of $\bar{S}$. Note that the system is slightly self-referential: there are $\langle b_i \rangle$ and $[b_i]$ modalities, coded by the bit patterns of the actions b_i themselves. It is clearly possible to choose n so large that all the necessary modalities may be coded as bit patterns.) To extract a HML formula φ from a process F, we let the bit pattern of F give the leading connective of φ; we write F *codes* σ (where σ is a connective) for $bitpat(F) = code(\sigma)$. Note that F *codes* σ may be expressed as a vector of $F \overset{b_i}{\rightarrow}$ and $F \overset{b_i}{\nrightarrow}$ conditions, and is meaningful in the antecedent of a GSOS rule.

The arguments, if any, of the main connective of φ will be represented by the children of F under d. For example, the process coding $\varphi_1 \wedge \varphi_2$ is the following. The root can take

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[4] F is a metavariable ranging over processes which we intend to be formulas. This is mnemonic only; F may be instantiated by any process. Indeed, this fact forces us to include many of the non-obvious features of the system.

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b_i-steps in the bit pattern for $\wedge$. It also has two d-descendants F_1 and F_2, which represent φ_1 and φ_2.

There is an obvious translation $\varphi \mapsto \varphi^*$ of HML formulas into processes. For example,

$$\begin{aligned}(\varphi_1 \wedge \varphi_2)^* &= \text{and}(\varphi_1^*, \varphi_2^*)\\ ((a)\varphi)^* &= \text{pos}_a(\varphi^*)\end{aligned}$$

The satisfaction operator $\text{Sat}(P, F)$ will attempt to interpret F as a formula of HML, as described above. $\text{Sat}(P, F)$ will produce actions e until it decides whether or not P satisfies the formula coded by F; it then produces an action, t or f as appropriate. If F does not code a formula, $\text{Sat}(P, F)$ may die without producing a t or f answer, or it may run forever. As it computes, it will make use of the computational Boolean operators $\triangle$ and $\triangledown$. For example, we will have the behavior:

$$\text{Sat}(P, (\varphi_1 \wedge \varphi_2)^*) \xrightarrow{e} \text{Sat}(P, \varphi_1^*) \, \triangle \, \text{Sat}(P, \varphi_2^*) \tag{9}$$

The computational "and" and "or," $P \triangle Q$ and $P \triangledown Q$, combine the e, t, and f actions produced by $\text{Sat}(P, F)$. $P \triangle Q$ runs P and Q synchronously until both have finished producing e's; then $P \triangle Q$ produces the conjunction of the truth signals produced by P and Q. $P \triangledown Q$ behaves similarly.

The system has some technical properties to facilitate the proof that bisimulation is a congruence. We call a process *crudely Boolean* if its synchronization tree consists of a single (finite or infinite) trace, with all actions before the final step being e's, and the final step labeled either e, t, or f. The proofs of congruence depend on having certain processes be crudely Boolean. $P \triangle Q$ and $P \triangledown Q$ are crudely Boolean whenever P and Q are; and furthermore, both operators are associative, commutative, and idempotent on crudely Boolean processes. $\text{Sat}(P, F)$ is a crude Boolean process for all P and F, in particular for those F which are not the translations of modal formulas.

It is also necessary that an F have a unique sequence of arguments to its coded main connective. $\bar{S}$ follows the convention that the d-descendants of a process are its arguments. However, the S operators in $\bar{S}$ will allow us to build processes which have too many or too few d-descendants; for example,

$$\text{and}(F_0, F_1) + \text{and}(F_2, F_3)$$

codes an ill-formed conjunction with four d-children. We will use global testing to make sure the rules use all the d-descendants as arguments. (Choosing, say, the first two descendants would lead to distinctions between bisimular processes, for bisimular processes may list their descendants in different orders.) The simple rule for conjunctions (equation

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9) gives the desired behavior on well-formed conjunctions; in general, we will treat all the d-children of a conjunction as conjuncts.

$$\frac{F \text{ codes } \wedge, \quad Children(F, d) = \langle F_1, \ldots, F_n \rangle}{\text{Sat}(P, F) \xrightarrow{e} \triangle_{i=1}^{n} \text{Sat}(P, F_i)}$$

where $\triangle_{i=1}^{n}$ is an iterated $\triangle$, and by convention $\triangle_{i=1}^{0} X_i$ is the process $t0$. Similarly, we treat all the d-children of a disjunction as disjuncts. A well-formed coded modality $(\langle a \rangle \varphi)^*$ or $([a]\varphi)^*$, has only one d-descendant. Again, to avoid distinguishing bisimular processes representing ill-formed formulas, the rules cannot ignore extra d-descendants; we use the disjunction of all the d-descendants (cf. rules (10), (11)), so that $\text{pos}_a F_1 + \text{pos}_a F_2$ is used in much the same way as $\text{pos}_a(\text{or}(F_1, F_2))$. The choice of disjunction was arbitrary; conjunction would have worked as well.

Note also that we are using an ordered global testing rules to make Sat(P,F) have only a single child. We fix some order e.g., lexicographic order on the children of each process when instantiating the antecedent of a global testing rule.

The full set of rules for this system is:

For each connective σ of HML over $\bar{S}$, index i such that the i^{th} bit of $code(\sigma)$ is 1, operator op corresponding to σ, and vector $\vec{X}$ of length equal to the arity of op,

$$\text{op}(\vec{X}) \xrightarrow{b_i} 0$$

In addition,

$$
\begin{aligned}
\text{and}(P, Q) &\xrightarrow{d} P \\
\text{and}(P, Q) &\xrightarrow{d} Q \\
\text{or}(P, Q) &\xrightarrow{d} P \\
\text{or}(P, Q) &\xrightarrow{d} Q \\
\text{pos}_a(P) &\xrightarrow{d} P \\
\text{nec}_a(P) &\xrightarrow{d} P
\end{aligned}
$$

The rules for $\text{Sat}(P, F)$ are given in Figure 8. Recall that the expression "P codes σ" is an abbreviation for a sequence of $P \xrightarrow{b_i} P_i$ and $P \xrightarrow{b_i} $ tests, and therefore is acceptable in the antecedent of a GSOS rule. Also recall that we are treating multiple arguments of a modality as their disjunction.

The computational Boolean operators are largely synchronous, to make them idempotent, associative, and commutative on crudely Boolean processes.

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$$\frac{F \text{ codes } \text{tt}}{\text{Sat}(P, F) \xrightarrow{t} 0}$$

$$\frac{F \text{ codes } \text{ff}}{\text{Sat}(P, F) \xrightarrow{f} 0}$$

$$\frac{F \text{ codes } \wedge, \quad Children(F, d) = \langle F_1, \ldots, F_n \rangle}{\text{Sat}(P, F) \xrightarrow{e} \triangle_{i=1}^n \text{Sat}(P, F_i)}$$

$$\frac{F \text{ codes } \vee, \quad Children(F, d) = \langle F_1, \ldots, F_n \rangle}{\text{Sat}(P, F) \xrightarrow{e} \triangledown_{i=1}^n \text{Sat}(P, F_i)}$$

$$\frac{F \text{ codes } \langle a \rangle, \quad Children(F, d) = \langle F_1, \ldots, F_n \rangle, \quad Children(P, a) = \langle P_1, \ldots, P_m \rangle}{\text{Sat}(P, F) \xrightarrow{e} \triangledown_{j=1}^m (\triangledown_{i=1}^n \text{Sat}(P_j, F_i))} \tag{10}$$

$$\frac{F \text{ codes } [a], \quad Children(F, d) = \langle F_1, \ldots, F_n \rangle, \quad Children(P, a) = \langle P_1, \ldots, P_m \rangle}{\text{Sat}(P, F) \xrightarrow{e} \triangle_{j=1}^m (\triangledown_{i=1}^n \text{Sat}(P_j, F_i))} \tag{11}$$

Figure 8: *Global-testing rules capturing bisimulation.*

$$\frac{P \xrightarrow{c} P', \quad Q \xrightarrow{c} Q'}{P \nabla Q \xrightarrow{c} P' \nabla Q', \quad P \triangle Q \xrightarrow{c} P' \triangle Q'}$$

$$\frac{P \xrightarrow{c} P', \quad Q \xrightarrow{c}}{P \nabla Q \xrightarrow{c} P' \nabla Q, \quad P \triangle Q \xrightarrow{c} P' \triangle Q}$$

$$\frac{P \xrightarrow{c}, \quad Q \xrightarrow{c} Q'}{P \nabla Q \xrightarrow{c} P \nabla Q', \quad P \triangle Q \xrightarrow{c} P \triangle Q'}$$

For $a', b' \in \{t, f\}$, let c' be their conjunction and d' their disjunction. We have the rules

$$\frac{P \xrightarrow{a'} P', \quad Q \xrightarrow{b'} Q'}{P \triangle Q \xrightarrow{c'} 0, \quad P \nabla Q \xrightarrow{d'} 0}$$

The following properties of $\bar{S}$ show that it is indeed what we want:

- There is a unique arrow relation compatible with these rules.

- The synchronization tree of each $\bar{S}$-process is computable relative to S, and finitely branching if all S-processes are finitely branching.

- For any formula φ of Hennessy-Milner logic over $\bar{S}$ and process P of $\bar{S}$, the process $\mathrm{Sat}(P, \varphi^*)$ has a trace ending in t iff $P \models \varphi$. So, trace congruence refines bisimulation.

- Bisimulation is a congruence relation with respect to all $\bar{S}$ operators.

- Hence, bisimulation coincides with trace congruence for this system.

The difficult property to verify is that bisimulation is a congruence. A sufficient condition to ensure that a system with global testing respects bisimulation is that the iterated operators in the consequents of global testing rules, in this case $\triangle$ and ∇, are commutative, associative, and idempotent. In our case, commutativity follows routinely from symmetries in the rules. In our system, $\triangle$ and ∇ are also idempotent and associative on crudely Boolean processes, which is all we need for the proof. Our rules were contrived to make this fact easily verified.

1. Trace Equivalence

2. Trace Congruence (CSP)

3. Refusal Testing ([22])

4. GSOS Congruence (Definition [?])

5. Bisimulation (CCS)

Figure 9: Successively Finer Equivalences on Processes

7   Conclusions

Should bisimulation play a significant role in process theory? It has many nice properties, a rich theory, and a tested methodology for verifying correctness of genuine, nontrivial protocols. Nevertheless, we find unconvincing the arguments for taking bisimulation as a primitive notion. We maintain that computational distinctions should be made only because of observable differences "at the terminal". Global testing systems which reduce bisimulation to such observations do not offer what we regard as a reasonable framework for defining operations on processes. Of course, the most persuasive pragmatic argument against bisimulation would be an independently interesting concurrent protocol verifiable using a methodology based on GSOS trace congruence but not even *correct* from the bisimulation view. Such an argument remains to be elaborated.

In any case, the development here clarifies the point of keeping to a GSOS discipline in specifying process behavior. It also motivates further investigation of a new congruence, namely GSOS trace congruence, which is finer than CSP or refusal testing congruence but coarser than bisimulation (cf Figure 9). Particularly noteworthy is the problem of axiomatizing the equational theory of finite trees with the operations + and a-prefixing under GSOS congruence.[5]

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[5] We will present our complete axiomatization of GSOS congruence in a later version of this paper.

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